

FIGURE 1

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ADDR	REGISTER CONTENTS	
16	LOCATION REGISTER	<u>120</u>
17	CONTROL REGISTER	<u>118</u>
18	16 BIT REGISTER	
19	16 BIT REGISTER	
20	16 BIT REGISTER	
21	16 BIT REGISTER	
22	16 BIT REGISTER	
23	16 BIT REGISTER	
24	BLOCK REGISTER 0	
25	BLOCK REGISTER 1	
26	BLOCK REGISTER 2	
27	BLOCK REGISTER 3	
28	BLOCK REGISTER 4	
29	BLOCK REGISTER 5	
30	BLOCK REGISTER 6	
31	BLOCK REGISTER 7	

110

112

FIGURE 2

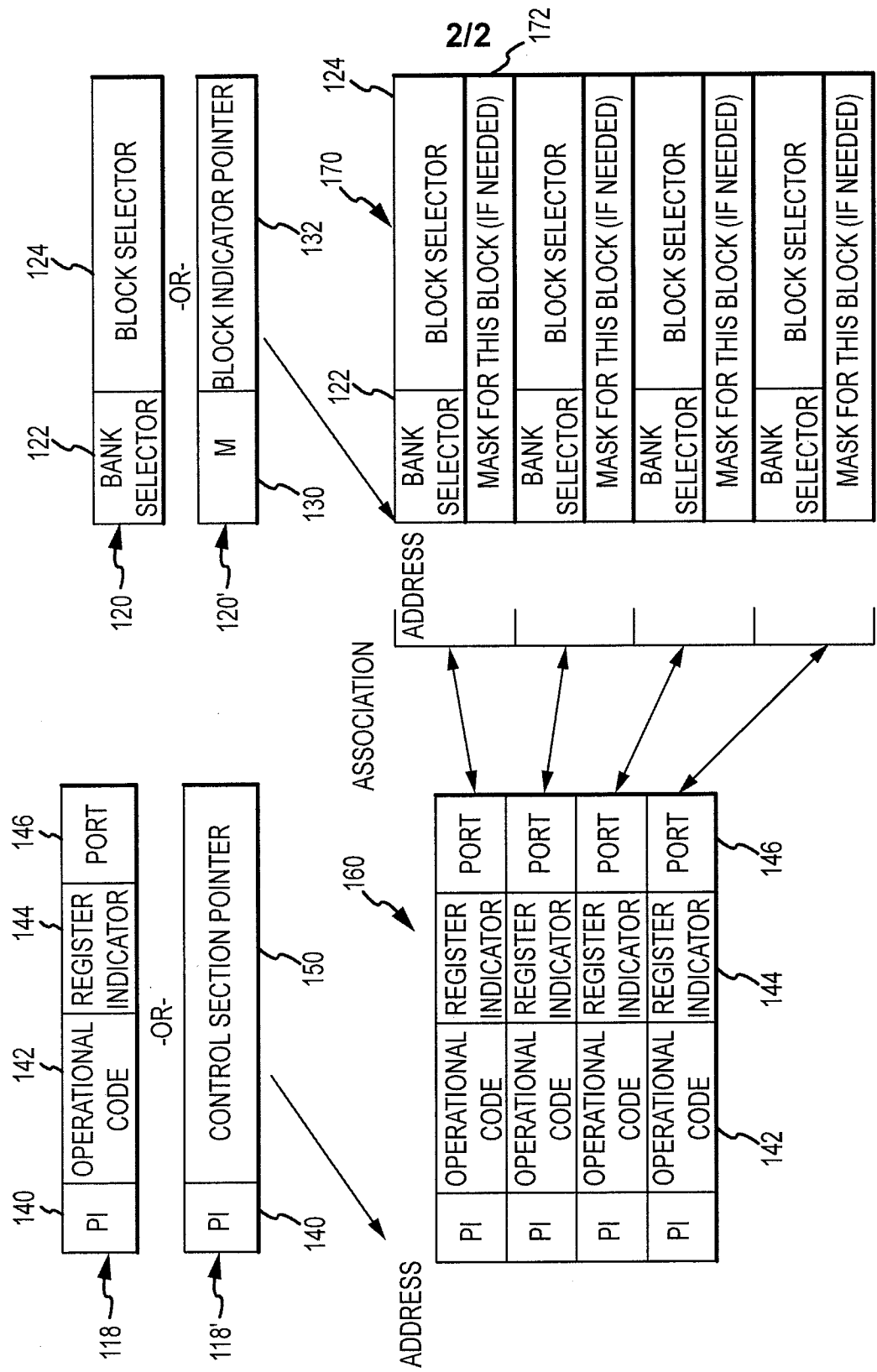


FIGURE 3